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September 5, 2007

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,091,108
Issued: August 15, 2006
For: **METHODS AND APPARATUSES FOR
MANUFACTURING ULTRA THIN DEVICE
LAYERS FOR INTEGRATED CIRCUIT
DEVICES**
Inventor: Tolchinsky, et al.
Our File No. 42390.P17299

Dear Sir:

Enclosed is the Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Edwin H. Taylor
Reg. No. 25,129

EHT/sn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,091,108
DATED : August 15, 2006
INVENTOR(S) : Tolchinsky et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, at line 53, delete "Si/1/pSi" and insert --Si/I/pSi--.

In column 5, at line 21, delete "Si/1/pSi" and insert --Si/I/pSi--.

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PATENT NO. 7,091,108

Certificate of Correction (PTO Form 1050)-Amended